**Verification of PI4ULS5V104 IBIS model**

1. **Introduction:**

To verify the correlation between the ibis model and hspice model, we need to do some simulations:

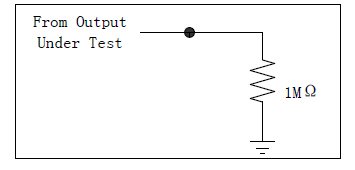
Simulation condition: (VCCA=3.3, VCCB=5.0, frequency=5megHz)

1. Add 1meg ohm resistor to ground;
2. Add 1meg ohm resistor and 15pF capacitor to ground;
3. Add 10k ohm resistor to ground;
4. Add 10k ohm resistor and 15pF capacitor to ground;
5. **Conclusion:**

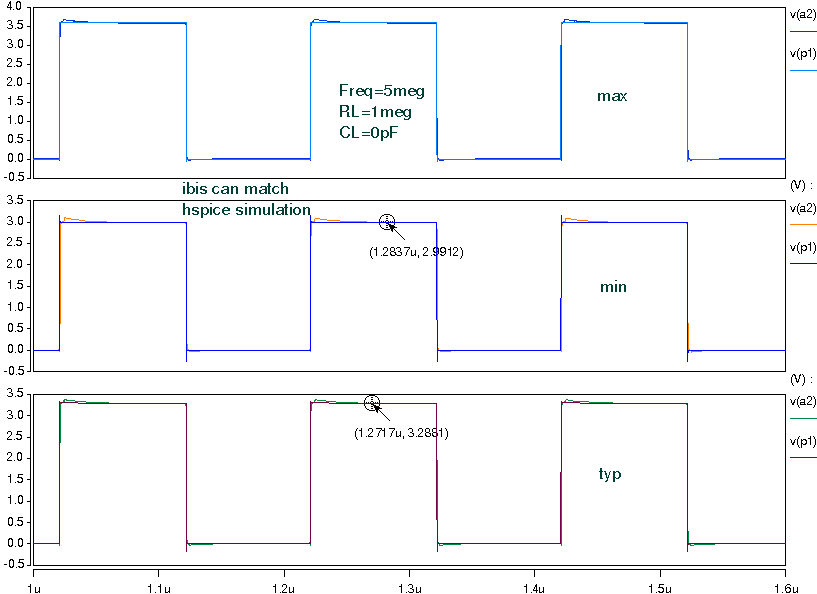
The simulated results show that the generated IBIS model can match well with the HSPICE model at different load conditions.

But there are still some **issues**:

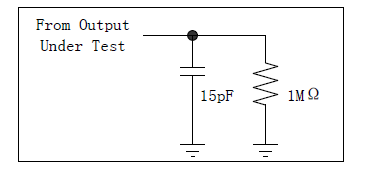
1. There is some undershoot at the fall-edge of IBIS simulation while it is disappeared in the HSPICE simulation.
2. **simulation:**
3. Add 1meg ohm resistor to ground;



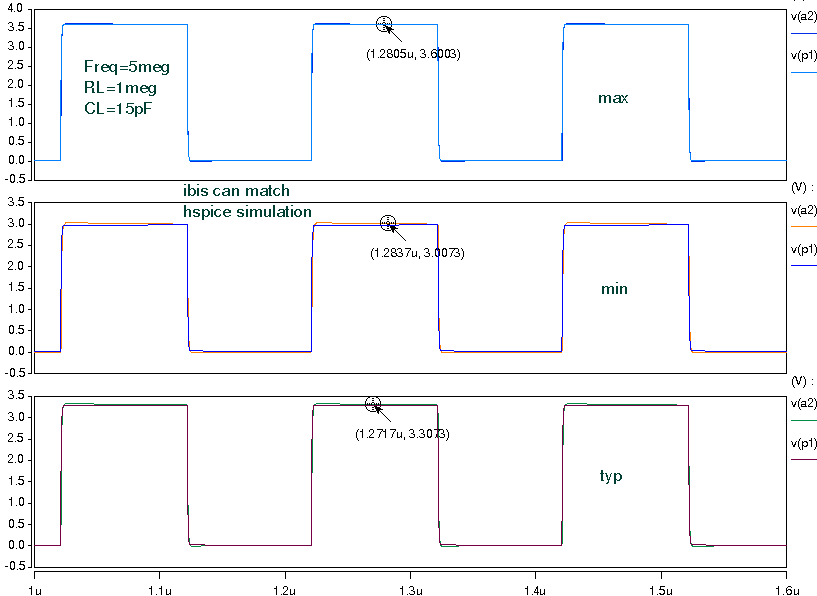
VCC=3.3V



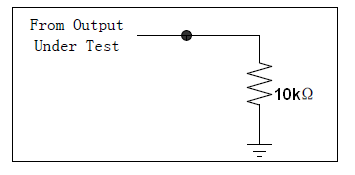
1. Add 1meg ohm resistor and 15pF capacitor to ground;



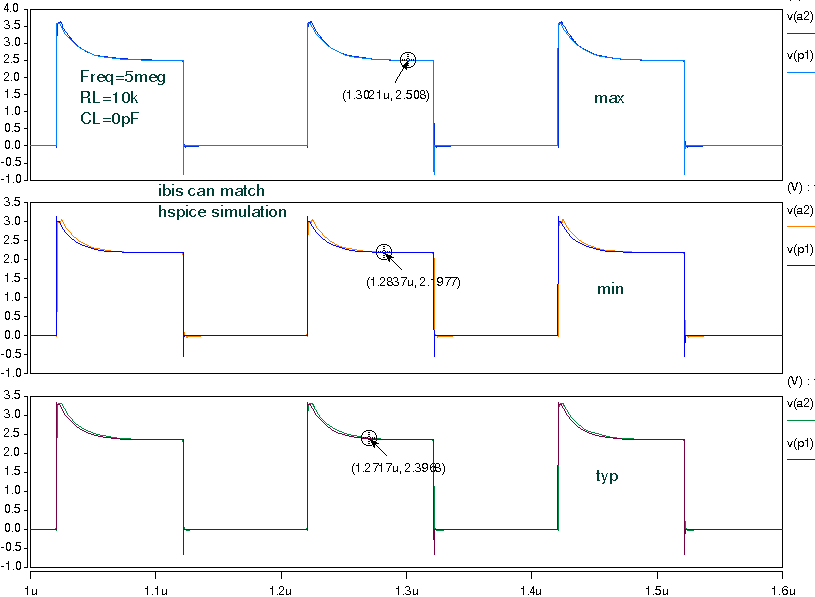
VCC=3.3V



1. Add 10k ohm resistor to ground;



VCC=3.3V



1. Add 10k ohm resistor and 15pF capacitor to ground;



VCC=3.3V

