VDD is +1.5V for PI2EQX6811

PIN CONFIGURATION for CONTROL

PIN NAME	PIN Control FUNCTION	I2C Control Function
P7:EN	With Internal 100k-ohm pull-up resistor High: Normal Operation Low: Power Down Mode	Same as Pin control function
P8:A0/B_EQ	Input Equalization for Channel B Tri-level control	I2C Programmable address bit A0
P9:A1/B_EM	Pre-emphasis control for Channel B Tri-level control	I2C Programmable address bit A1
P10:I2C_EN#	I2C Enable High: pin control Low: I2C control	Same as Pin control function
P17: SDATA/A_EQ	Input Equalization for Channel A Tri-level control by JP25 Setting Value same as P8	I2C Data Line
P18:APD_EN#	Auto slumber mode Enable High: disable Low: enable	Don't Care
P19: SCLK/A_EM	Pre-emphasis control for Channel A Tri-level control by JP27 Setting Value same as P9	I2C Clock Line
P20:SW	Output Swing control for Channel A&B Tri-level control	Don't Care

JP21	Input Equalization for Channel B
GND	8dB
Open	4dB
VDD	16dB

JP22	Input Equalization for Channel B
GND	2dB
Open	0dB
VDD	3.5dB

JP24	Output Swing for Channel A&B mV(Vtx_diff_pp) at 3Gb/s
GND	667
Open	533
VDD	900
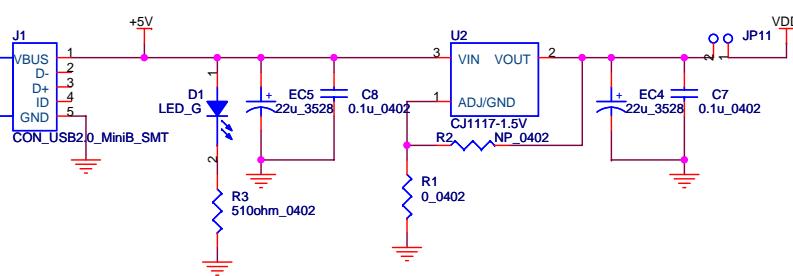
Board Name:

PI2EQX6811ZDE
Evaluation Board Rev.A

U3 — PI2EQX6811ZDE@TQFN20

EC1 22u_3528 · C1 0.1u_0402 · C2 0.1u_0402 · C3 0.1u_0402 · C4 0.1u_0402

R30 10k_0402 · R29 10k_0402 · C6 0.1u_0402 · J2 4p-2.54mm Connector

CON_SATA_SMT (JP12) · CON_SATA_SMT (JP13)

R6 0_0402 · R7 0_0402 · C15 10n_0402 · C16 10n_0402 · R4 NP/0_0402

C11 10n_0402 · C12 10n_0402 · R8 0_0402 · R9 0_0402 · R5 NP/0_0402

J1 CON_USB2.0_MiniB_SMT · D1 LED_G · EC5 22u_3528 · C8 0.1u_0402 · U2 CJ1117-1.5V · EC4 22u_3528 · C7 0.1u_0402

R3 510ohm_0402 · R1 0_0402 · R2 NP_0402