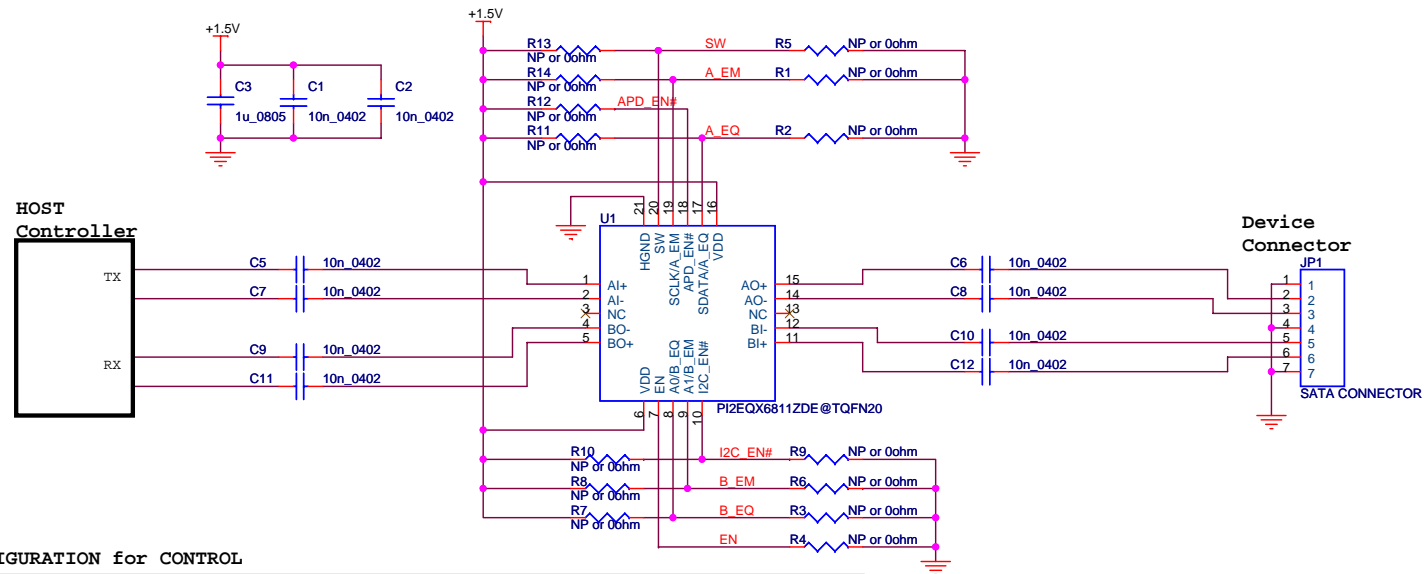


Application Circuit for PIN Control



PIN CONFIGURATION for CONTROL

PIN NAME	PIN Control FUNCTION								
P7:EN	With Internal 100k-ohm pull-up resistor High: Normal Operation Low: Power Down Mode R4=NP								
P8:A0/B_EQ	Input Equalization for Channel B Tri-level control <table border="1"> <thead> <tr> <th colspan="2">Input Equalization for Channel B</th></tr> </thead> <tbody> <tr> <td>GND</td><td>8dB R3=0ohm, R7=NP</td></tr> <tr> <td>Open</td><td>4dB R3=NP, R7=NP</td></tr> <tr> <td>VDD</td><td>16dB R3=NP, R7=0ohm</td></tr> </tbody> </table>	Input Equalization for Channel B		GND	8dB R3=0ohm, R7=NP	Open	4dB R3=NP, R7=NP	VDD	16dB R3=NP, R7=0ohm
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P9:A1/B_EM	Pre-emphasis control for Channel B Tri-level control <table border="1"> <thead> <tr> <th colspan="2">Input Equalization for Channel B</th></tr> </thead> <tbody> <tr> <td>GND</td><td>2dB R6=0ohm, R8=NP</td></tr> <tr> <td>Open</td><td>0dB R6=NP, R8=NP</td></tr> <tr> <td>VDD</td><td>3.5dB R6=NP, R8=0ohm</td></tr> </tbody> </table>	Input Equalization for Channel B		GND	2dB R6=0ohm, R8=NP	Open	0dB R6=NP, R8=NP	VDD	3.5dB R6=NP, R8=0ohm
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GND	2dB R6=0ohm, R8=NP								
Open	0dB R6=NP, R8=NP								
VDD	3.5dB R6=NP, R8=0ohm								
P10:I2C_EN#	I2C Enable High: pin control R10=0ohm, R9=NP Low: I2C control								
P17:SDATA/A_EQ	Input Equalization for Channel A R2 and R11 Tri-level control by R2 and R11 Setting Value same as P8								
P18:APD_EN#	Auto slumber mode Enable High: disable Low: enable								
P19:SCLK/A_EM	Pre-emphasis control for Channel A R1 and R14 Tri-level control by R1 and R14 Setting Value same as P9								
P20:SW	Output Swing control for Channel A&B Tri-level control <table border="1"> <thead> <tr> <th colspan="2">Output Swing for Channel A&B mV(Vtx_diff_pp) at 3Gb/s</th></tr> </thead> <tbody> <tr> <td>GND</td><td>667 R5=0ohm, R13=NP</td></tr> <tr> <td>Open</td><td>533 R5=NP, R13=NP</td></tr> <tr> <td>VDD</td><td>900 R5=NP, R13=0ohm</td></tr> </tbody> </table>	Output Swing for Channel A&B mV(Vtx_diff_pp) at 3Gb/s		GND	667 R5=0ohm, R13=NP	Open	533 R5=NP, R13=NP	VDD	900 R5=NP, R13=0ohm
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GND	667 R5=0ohm, R13=NP								
Open	533 R5=NP, R13=NP								
VDD	900 R5=NP, R13=0ohm								

PIN NAME	PIN Control FUNCTION	I2C Control Function																
P7:EN	With Internal 100K-ohm pull-up resistor High: Normal Operation Low: Power Down Mode R4=NP	Same as Pin control function R4=NP																
P8:A0/B_EQ	Input Equalization for Channel B Tri-level control <table border="1" style="margin: 5px auto; width: 60%;"> <thead> <tr> <th></th><th colspan="3">Input Equalization for Channel B</th></tr> </thead> <tbody> <tr> <td>GND</td><td>8dB</td><td colspan="2">R3=0ohm, R7=NP</td></tr> <tr> <td>Open</td><td>4dB</td><td colspan="2">R3=NP, R7=NP</td></tr> <tr> <td>VDD</td><td>16dB</td><td colspan="2">R3=NP, R7=0ohm</td></tr> </tbody> </table>		Input Equalization for Channel B			GND	8dB	R3=0ohm, R7=NP		Open	4dB	R3=NP, R7=NP		VDD	16dB	R3=NP, R7=0ohm		I2C Programmable address bit A0 R3 and R7 for A0 selection
	Input Equalization for Channel B																	
GND	8dB	R3=0ohm, R7=NP																
Open	4dB	R3=NP, R7=NP																
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	Input Equalization for Channel B																	
GND	2dB	R6=0ohm, R8=NP																
Open	0dB	R6=NP, R8=NP																
VDD	3.5dB	R6=NP, R8=0ohm																
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P17:SDATA/A_EQ	Input Equalization for Channel A Tri-level control by R2 and R11 Setting Value same as P8 R2 and R11 R18=NP	I2C Data Line R2 and R11=NP R18=0ohm																
P18:APD_EN#	Auto slumber mode Enable High: disable Low: enable	Don't Care																
P19:SCLK/A_EM	Pre-emphasis control for Channel A Tri-level control by R1 and R14 Setting Value same as P9 R1 and R14 R17=NP	I2C Clock Line R1 and R14 R17=0ohm																
P20:SW	Output Swing control for Channel A&B Tri-level control <table border="1" style="margin: 5px auto; width: 60%;"> <thead> <tr> <th></th><th colspan="3">Output Swing for Channel A&B mV(Vtx_diff_pp) at 3Gb/s</th></tr> </thead> <tbody> <tr> <td>GND</td><td>667</td><td colspan="2">R5=0ohm, R13=NP</td></tr> <tr> <td>Open</td><td>533</td><td colspan="2">R5=NP, R13=NP</td></tr> <tr> <td>VDD</td><td>900</td><td colspan="2">R5=NP, R13=0ohm</td></tr> </tbody> </table>		Output Swing for Channel A&B mV(Vtx_diff_pp) at 3Gb/s			GND	667	R5=0ohm, R13=NP		Open	533	R5=NP, R13=NP		VDD	900	R5=NP, R13=0ohm		Don't Care
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