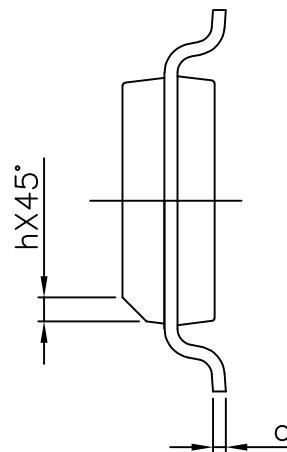
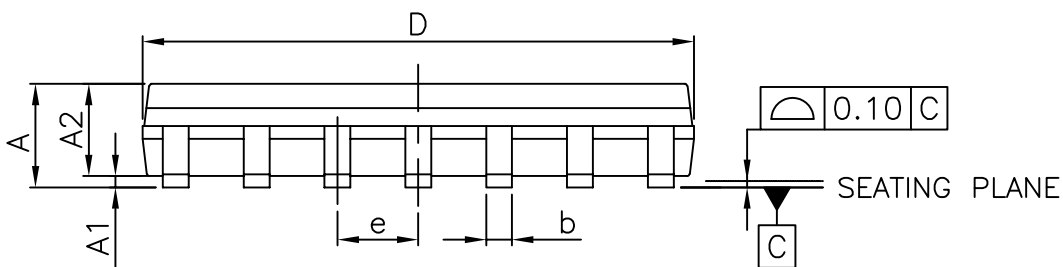


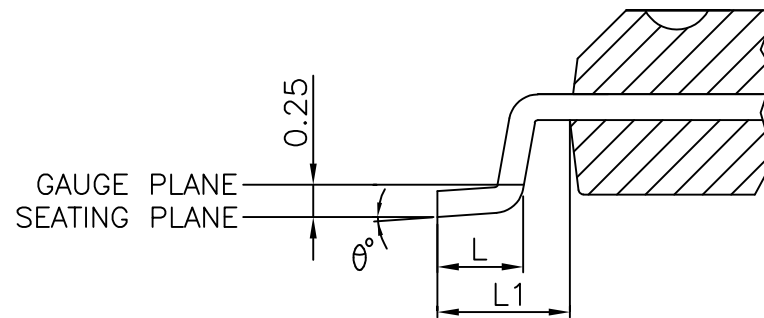
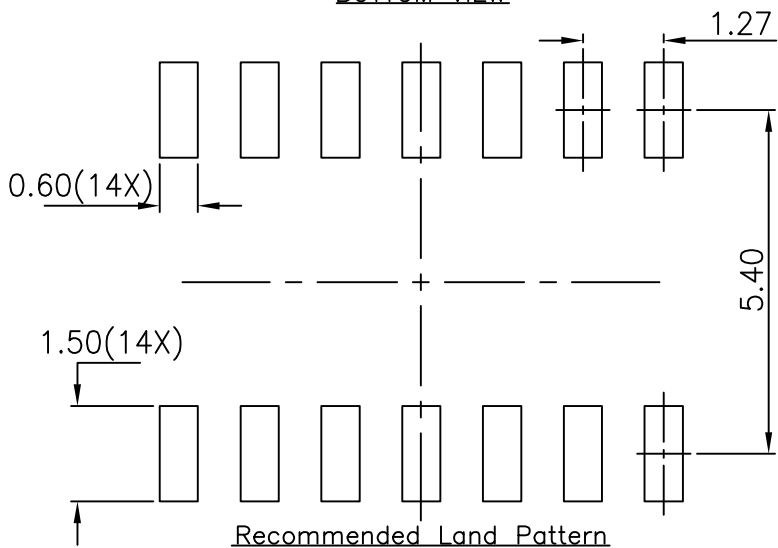
TOP VIEW



SYMBOLS	MIN.	NOM.	MAX.
A	—	—	1.75
A1	0.10	—	0.25
A2	1.25	—	—
b	0.31	—	0.51
c	0.10	—	0.25
D	8.55	8.65	8.75
E	5.80	6.00	6.20
E1	3.70	3.90	4.10
e	1.27 BSC		
L	0.40	—	1.27
L1	1.04 REF		
h	0.25	—	0.50
θ°	0	—	8



BOTTOM VIEW



- NOTES:
 1. ALL DIMENSIONS ARE IN mm. ANGLES IN DEGREES.
 2. JEDEC MS-012
 3. DIMENSIONS DOES NOT INCLUDE MOLD FLASH, PROTRUSIONS OR GATE BURRS.

DIODES **PERICOM** A PRODUCT LINE OF DIODES INCORPORATED
 INCORPORATED ENABLING SERIAL CONNECTIVITY

DATE: 05/15/23

DESCRIPTION: 14-Pin, 150mil Wide SOIC

PACKAGE CODE: W (W14)

DOCUMENT CONTROL #: PD-1002

REVISION: G