

PI7C9X20303SLC and PI7C9X20404SLC PCIe® Packet Switch Power Consumption in Normal Mode

Power Consumption of PI7C9X20303SLC and PI7C9X20404SLC

The following table lists the details of the power consumption of each of the power rails that are used by the PI7C9X20303SLC and PI7C9X20404SLC devices. This information can be taken into consideration for system design and power budget.

Normal mode	Empty				Full-loading			
	PWR_SAV Disable		PWR_SAV Enable		PWR_SAV Disable		PWR_SAV Enable	
	mA	mW	mA	mW	mA	mW	mA	mW
1.0VDDC	279.40	279.40	134.20	134.20	410.30	410.30	410.30	410.30
1.0VDDAUX	62.70	62.70	63.80	63.80	61.60	61.60	61.60	61.60
1.0VDDA	92.40	92.40	92.40	92.40	112.20	112.20	112.20	112.20
1.5VTT	39.60	59.40	39.60	59.40	107.80	161.70	107.80	161.70
3.3VDDR	1.54	5.08	1.71	5.63	1.32	4.36	1.30	4.28
3.3VAUX	0.01	0.04	0.01	0.04	0.01	0.04	0.01	0.04
Total (mW)	499.02		355.46		750.19		750.12	

Test Conditions:

- Platform: MSI MS-7360 Intel ICH9, Intel Celeron CPU 430 1.8G, DDR2 512M
- Empty: No endpoints were present on the system
- Full-loading: two SK-9E21D Ethernet cards were connected with PI7C9X20303SLC, three SK-9E21D Ethernet cards were connected with PI7C9X20404SLC, and "IO Chariot" was executed.